

IN THE CLAIMS

The pending claims are as follows:

1. (Previously Presented) A circuit for generating a composite output picture signal such as a picture signal comprising a PIP (Picture-in-Picture) signal, or a DW (Double Window) signal, on the basis of at least two input picture signals, the circuit

5 comprising:

color decoding means for decoding the at least two input picture signals, the color decoding means comprising combining means for losslessly combining the at least two input picture signals into one combined picture signal, and one color decoder for
10 decoding the one combined picture signal to form a single decoded combined picture signal; and

composing means for generating the composite output picture signal on the basis of the single decoded combined picture signal.

2. (Previously Presented) The circuit as claimed in claim 1, wherein the combining means comprises a multiplexer for time-multiplexing the at least two input picture signals to form the one combined picture signal.

3. (Previously Presented) The circuit as claimed in claim 2,
wherein the multiplexer comprises a buffer-memory.

4. (Previously Presented) The circuit as claimed in claim 1,
wherein the composing means comprises a de-multiplexer for time de-
multiplexing the decoded combined picture signal into at least two
decoded picture signals for further processing to form the
5 composite output picture signal.

5. (Previously Presented) The circuit as claimed in claim 4,
wherein the composing means further comprises a memory and a micro-
processor for further processing the at least two decoded picture
signals.

6. (Previously Presented) A television apparatus comprising:
the circuit as claimed in claim 1; and
a display device for displaying the composite output
picture signal.

7. (Previously Presented) A video tape recorder comprising:
a circuit as claimed in claim 1.